



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/729,084	12/05/2003	Kuo-Chi Tu	TSM03-0498	8825
43859	7590	11/01/2005	EXAMINER	
SLATER & MATSIL, L.L.P. 17950 PRESTON ROAD, SUITE 1000 DALLAS, TX 75252			GARCIA, JOANNIE A	
			ART UNIT	PAPER NUMBER
			2823	

DATE MAILED: 11/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.



## UNITED STATES DEPARTMENT OF COMMERCE

## U.S. Patent and Trademark Office

Address: COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

10/ 729 084

APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION	ATTORNEY DOCKET NO.
---------------------------------	-------------	---	---------------------

EXAMINER

ART UNIT

PAPER

20051027

DATE MAILED:

Please find below and/or attached an Office communication concerning this application or proceeding.

## Commissioner for Patents

The reply filed on 08-12-05 is not fully responsive to the prior Office Action because of the following omission(s) or matter(s): in claim 20, line 7, deletion of "mask" after "forming a", and insertion of --first nitride-- before "layer over"; in claim 20, line 8, insertion of --depositing a first photoresist layer over the first nitride layer--; in claim 20, line 9, insertion of --patterning the first photoresist layer with-- before "a pattern for at least", and deletion of "the mask layer comprising"; in claim 20, lines 12-14, insertion of --using the first photoresist layer to pattern the first nitride layer with the pattern for the at least one bottom electrode in the at least one capacitor region and the pattern for at least one floating gate in the at least one transistor region"; and in claim 20, line 19, deletion of "mask" after "removing the", and insertion of --first nitride-- before "layer, leaving portions of the", were not properly indicated. Applicant is required to carefully inspect future amendments for full compliance with US 37 CFR 1.121. See 37 CFR 1.111. Since the above-mentioned reply appears to be bona fide, applicant is given ONE (1) MONTH or THIRTY (30) DAYS from the mailing date of this notice, whichever is longer, within which to supply the omission or correction in order to avoid abandonment. EXTENSIONS OF THIS TIME PERIOD MAY BE GRANTED UNDER 37 CFR 1.136(a).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joannie García whose telephone number is (571) 272-1861. The examiner can normally be reached on Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Smith, can be reached on (571) 272-1907. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
JAG

September 21, 2005

GFourson

  
George Fourson  
Primary Examiner  
Art Unit 2823